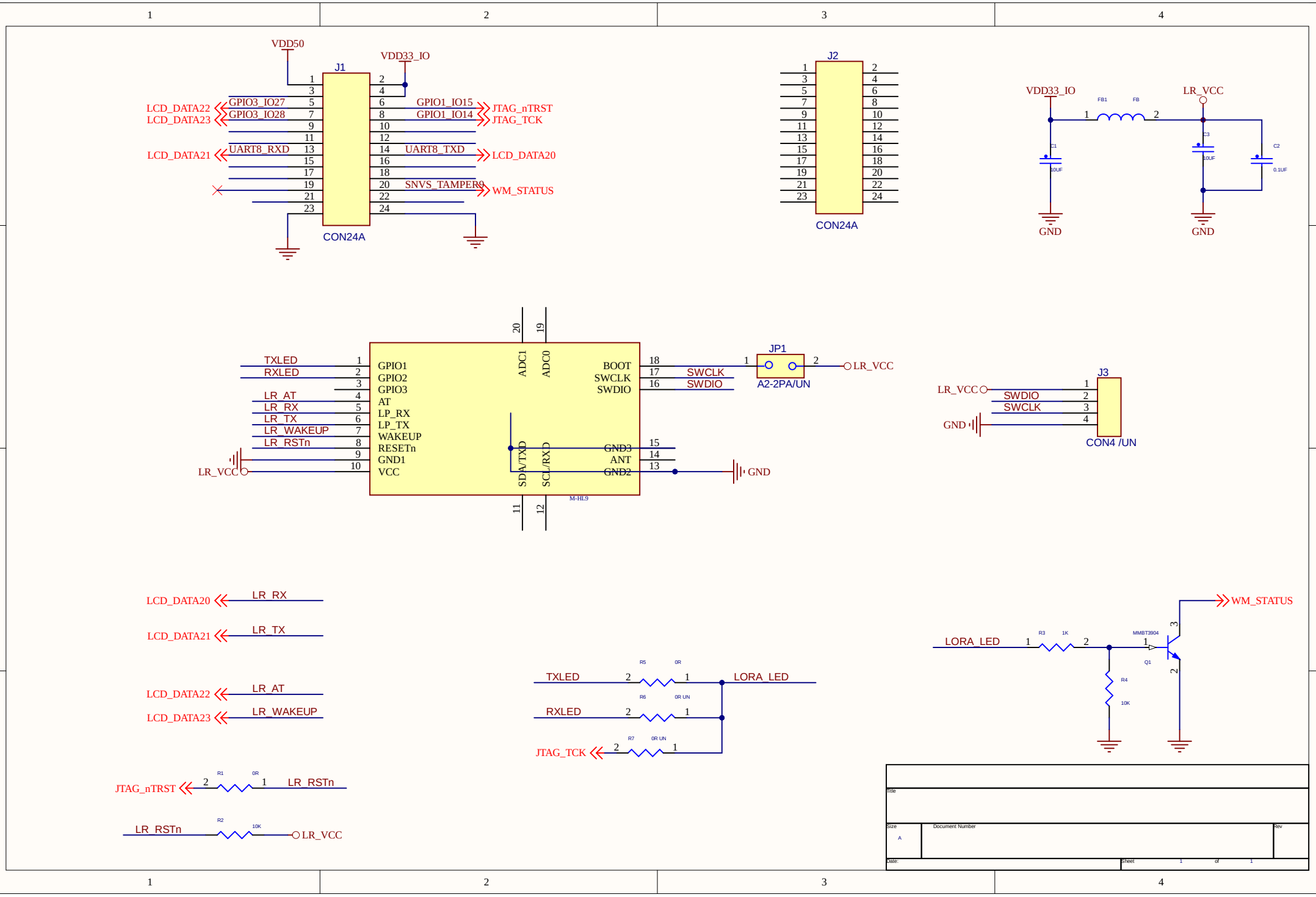
VDD50
VDD33_IO
J1
CON24A
LCD_DATA22
LCD_DATA23
LCD_DATA21
GPIO3_IO27
GPIO3_IO28
UART8_RXD
GPIO1_IO15
GPIO1_IO14
UART8_TXD
SNVS_TAMPER9
JTAG_nTRST
JTAG_TCK
LCD_DATA20
WM_STATUS
J2
CON24A
VDD33_IO
LR_VCC
FB1
FB
C1
10UF
C3
10UF
C2
0.1UF
GND
GND
TXLED
RXLED
LR_AT
LR_RX
LR_TX
LR_WAKEUP
LR_RSTn
LR_VCC
GPIO1
GPIO2
GPIO3
AT
LP_RX
LP_TX
WAKEUP
RESETn
GND1
VCC
ADC1
ADC0
BOOT
SWCLK
SWDIO
SDA/TXD
SCL/RXD
GND3
ANT
GND2
M-HL9
SWCLK
SWDIO
JP1
A2-2PA/UN
LR_VCC
GND
LR_VCC
SWDIO
SWCLK
GND
J3
CON4 /UN
LCD_DATA20
LR_RX
LCD_DATA21
LR_TX
LCD_DATA22
LR_AT
LCD_DATA23
LR_WAKEUP
TXLED
RXLED
JTAG_TCK
R5
0R
R6
0R UN
R7
0R UN
LORA_LED
WM_STATUS
LORA_LED
R3
1K
MMBT3904
Q1
R4
10K
JTAG_nTRST
R1
0R
LR_RSTn
LR_RSTn
R2
10K
LR_VCC
Title
Size
A
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Rev
Date:
Sheet
1
of
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